



The RISC-V

Multi-Media Decoding Platform SoC

Overview

D1 is an advanced application processor designed for RISC-V Multi-Media decoding platform. It integrates a 64-bit XuanTie C906 RISC-V CPU and a HiFi4 DSP to provide the high-efficient computing power. D1 supports full format decoding such as H.265, H.264, MPEG-1/2/4, JPEG, VC1, and so on. The independent encoder can encode in JPEG or MJPEG. Integrated multi ADCs/DACs and I2S/PCM/DMIC/OWA audio interfaces can work seamlessly with the CPU to accelerate multimedia algorithms and improve the user experience. D1 supports RGB/LVDS/MIPI DSI/HDMI/CVBS OUT display output interfaces to meet the requirements of the different screen display. D1 comes with extensive connectivity and interfaces, such as USB, SDIO, EMAC, TWI, UART, SPI, PWM, GPADC, LRADC, TPADC, IR TX&RX, and so on. Besides, D1 can connect with other different peripherals like WiFi and BT via SDIO and UART.

Highlights

- D1 integrates 64-bit XuanTie C906 RISC-V CPU to provide energy-efficient and stable computing power.
- D1 integrates H.265/H.264 4K decoding and SmartColor2.0 post processing to deliver the perfect video entertainment experience.
- D1 supports high performance 3 ADCs, 2 DACs, 3 I2S/PCM, 8 digital microphones to provide the perfect voice interaction solutions.
- Rich peripheral interfaces, such as RGB, LVDS, MIPI DSI, USB, SDIO, EMAC, TWI, UART, SPI, PWM, GPADC,
 LRADC, TPADC, IR TX&RX, and so on, greatly facilitate product expansion.
- The advanced process design with lower voltage and lower leakage, the power optimization design for typical scenes, and the enhanced heat dissipation package improve the heating experience of the product.
- Industrial level working temperature, 10-years chip life.

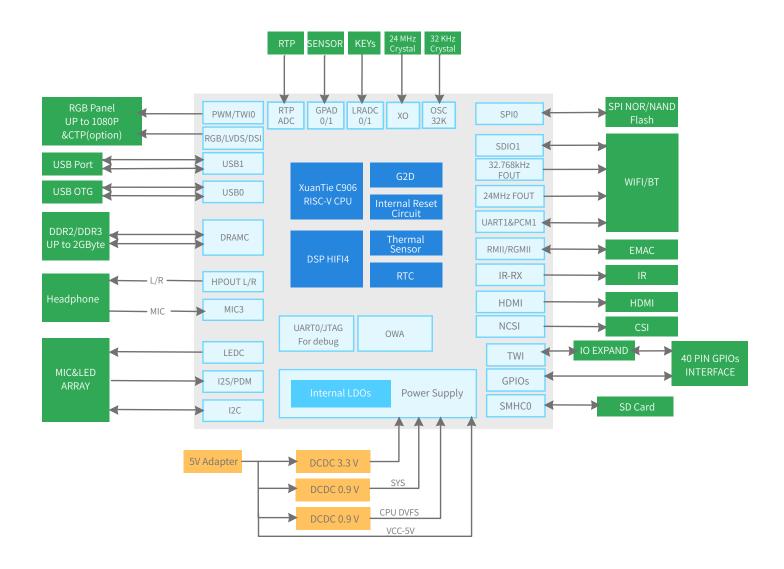
Features

CPU	• XuanTie C906 RISC-V CPU • 32 KB I-cache + 32 KB D-cache				
DSP	 HiFi4 DSP 32 KB I-cache + 32 KB D-cache 64 KB I-ram + 64 KB D-ram 				
Memory	• DDR2/DDR3, up to 2 GB • SD3.0/eMMC 5.0, SPI Nor/Nand Flash				
Video Engine	 Video decoding H.265 up to 1080p@60fps, or 4K@30fps H.264 up to 1080p@60fps, or 4K@24fps MPEG-1/2/4, JPEG, VC1 up to 1080p@60fps Video encoding JPEG/MJPEG up to 1080p@60fps Supports input picture scaler up/down 				
Display Engine	 Allwinner SmartColor2.0 post processing for an excellent display experience Supports de-interlace (DI) up to 1080p@60fps Supports G2D hardware accelerator including rotate, mixer, lbc decompression functions 				
Video OUT	 RGB LCD output interface up to 1920 x 1080@60fps Dual link LVDS interface up to 1920 x 1080@60fps 4-lane MIPI DSI interface up to 1920 x 1080@60fps HDMI V1.4 output interface up to 4K@30fps CVBS OUT interface, supporting NTSC and PAL format 				
Video IN	8-bit parallel CSI interface CVBS IN interface, supporting NTSC and PAL format				
Audio	 2 DACs and 3 ADCs Analog audio interfaces: MICIN1P/N, MICIN2P/N, MICIN3P/N, FMINL/R, LINEINL/R, LINEOUTLP/N, LINEOUTRP/N, HPOUTL/R Digital audio interfaces: I2S/PCM, DMIC, OWA IN/OUT 				
Connectivity	 USB2.0 OTG, USB2.0 Host SDIO 3.0, SPI x 2, UART x 6, TWI x 4 PWM (8-ch), GPADC (2-ch), LRADC (1-ch), TPADC (4-ch), IR TX&RX 10/100/1000M EMAC with RMII and RGMII interfaces 				
Package	• LFBGA 337 balls, 13 mm x 13 mm				

Block Diagram

Video Input	XuanTie C906 RISC-V CPU		HiFi4 DSP		Connectivity
Parallel CSI			I-cache 32 KB	D-cache 32 KB	USB2.0 OTG
CVBS IN	I-cache 32 KB	D-cache 32 KB	I-ram 64 KB	D-ram 64 KB	USB2.0 HOST
Video Output			04 NB	04 NB	SDI03.0
MIPI DSI	Display Engine		Interna	l System	SPI x2 (Supports SPI Nand/Nor Flash)
RGB	DE		CC	CU	TWI x4
Dual link LVDS	91		D	MA	UART x6
HDMI	DI		Therma	al Sensor	100M/1000M EMAC
CVBS OUT	G	2D	Tiı	mer	GPADC (2-ch)
A 22			High Spe	eed Timer	TPADC (4-ch)
Audio Audio Codec	Video	Engine	101	4MU	LRADC (1-ch)
	Video Decoding		TOTVINO		PWM (8-ch)
I2S/PCM x 3			Memory		LEDC
DMIC	Video Encoding JPEG/MJPEG		DDR2/	/DDR3	IR TX
OWA IN/OUT			SD3.0/e	MMC5.0	IR RX

Application Diagram



ABOUT ALLWINNER

Allwinner Technology is a leading fabless design company dedicated to smart application processor SoCs and smart analog ICs. Its product line includes multi-core application processors for smart devices and smart power management ICs used by brands worldwide.

With its focus on cutting edge UHD video processing, high performance multi-core CPU/GPU integration, and ultra-low power consumption, Allwinner Technology is a mainstream solution provider for the global tablet, internet TV, smart home device, automotive in-dash device, smart power management, and mobile connected device markets. Allwinner Technology is headquartered in Zhuhai, China.

CONTACT US

For more product info, please contact service@allwinnertech.com, or scan the QR code to follow us on Wechat.



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