



Multi-Interface

Quad-CorePlatform-Level Processor

Overview

The Allwinner V40 is a Quad-core ARM Cortex[™] -A7 processor targeted at the smart rearview mirror market. With multiple interface and high cost effectiveness, it could also apply to the market of automotive, POS terminal, digital signage and smart home as a platform-level processor.

Highlights



Based on a Quad-core ARM Cortex[™]-A7 CPU architecture and Mali 400 MP2 GPU, which providing the powerful processing and display performance.



Supports multiple 32-bit DRAM types as well as the dynamic frequency adjustment, give consideration to both scenarios used bandwidth and power requirements.



Supports the multiple output interface and two independent display channels, which could satisfy many kinds of products and market applications.



Supports two-lane CSI image sensor and four-lane TV In&Out, which meet the 360 panoramic demand of automotive products.



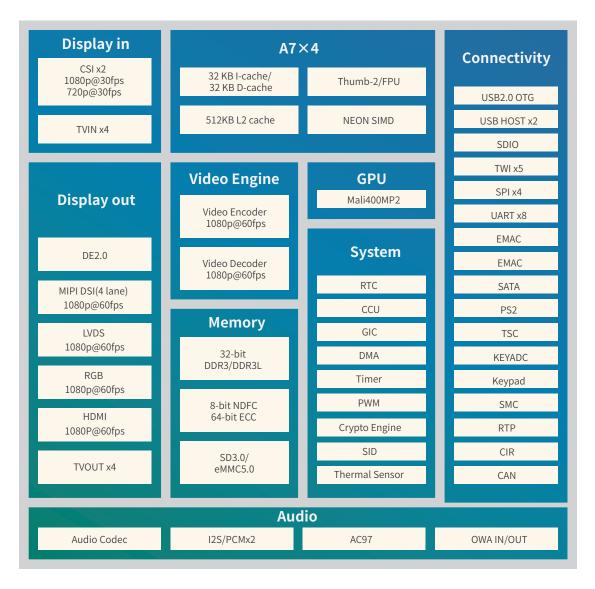
Supports Android 4.4 System and above.

Features

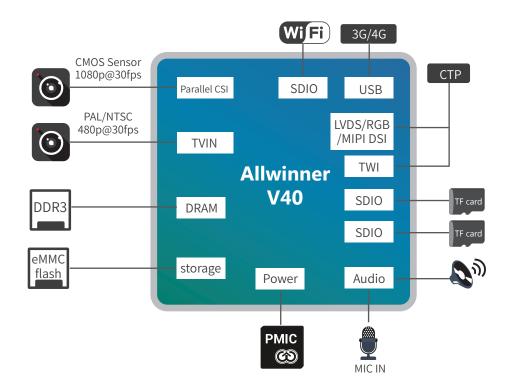
CPU	• Quad-core ARM Cortex™-A7 Processor
GPU	• Mali400 MP2
Memory	 Supports 32-bit DDR3/DDR3L/LPDDR2/LPDDR3 up to 576MHz Supports system boot from NAND Flash, eMMC, SD/TF card and SPI Nor Flash
Audio	• Up to two kits of I2S/PCM interfaces, eight channels of Time Division Multiplexing(TDM) with sampling precision up to 32bit/192KHz

Display	 Supports 4 lanes MIPI DSI up to 1080p@60fps Supports LVDS interface up to 1920 x 1080@60fps Supports RGB interface up to 1920 x 1080@60fps Supports TV In&Out Supports HDMI 1.4 transmitter
Camera	 Supports dual CMOS sensor parallel interfaces up to 1080p@30fps Supports BT656, BT1120 and 24-bit YUV444/RGB Supports two-lane input simultaneously
Video	 Supports decoding speed up to 1080p@60fps Supports H.264 HP encoding up to 1080p@60fps, JPEG baseline: picture size up to 4096 x 4096
External Peripherals	 USB x 3, SATA x 1 UART x 8, SPI x 4, Smart Card x 1, TS x 2, TWI x 5 MAC x 2, PS2 x 2, KEYPAD x 1(8 row 8 column), IR-RX x 2, KEYADC x 2, RTP
Package	• FBGA 468 balls,0.65mm ball pitch, 16x16 mm

Block Diagram



Application Diagram



ABOUT ALLWINNER

Allwinner Technology is a leading fabless design company dedicated to smart application processor SoCs and smart analog ICs. Its product line includes multi-core application processors for smart devices and smart power management ICs used by brands worldwide.

With its focus on cutting edge UHD video processing, high performance multi-core CPU/GPU integration, and ultra-low power consumption, Allwinner Technology is a mainstream solution provider for the global tablet, internet TV, smart home device, automotive in-dash device, smart power management, and mobile connected device markets. Allwinner Technology is headquartered in Zhuhai, China.

CONTACT US

For more product info, please contact service@allwinnertech.com, or scan the QR code to follow us on Wechat.

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